

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	83	"330"/\$.CCLS. AND (photo near1 (detect\$3 OR diode)) and ((diode near1 connected) or (current near1 mirror)) and capacit\$5	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/21 09:55
L6	566	(330/288 or 330/257 or 330/308 or 330/59 or 330/277 or 330/296 or 250/214A or 250/214AG).CCLS. and (current near1 mirror) and capacit\$5 and load and current and (ground or (reference near1 voltage))	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/21 10:02
L7	405	(330/288 or 330/257 or 330/308 or 330/59 or 330/277 or 330/296 or 250/214A or 250/214AG).CCLS. and (diode near1 connected) and capacit\$5 and load and current and (ground or (reference near1 voltage))	US-PGPUB; USPAT; USOCR	OR	ON	2005/11/21 10:08
L9	61	((diode near1 connected) and (current near1 mirror)) and capacit\$5	EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/21 10:08
S1	420	(330/308).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/22 13:36
S2	576	(330/59).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/01 13:45
S5	684	(330/288).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/01 13:45
S6	223	(250/214AG).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/01 13:46
S7	223	(250/214AG).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/01 13:46
S8	53	330/308.ccls. and capacit\$5 and ((common near1 gate) or (common near1 base))	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/01 14:15
S12	20	330/288.ccls. and (capacit\$5 same ((common near1 gate) or (common near1 base)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/01 14:20
S13	865	(330/296).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/01 14:17
S15	8	330/59.ccls. and (capacit\$5 same ((common near1 gate) or (common near1 base)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/01 14:22

S16	24	(250/214A or 250/214AG).ccls. and (capacit\$5 same ((common near1 gate) or (common near1 base)))	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/01 14:22
S19	341	(photo near1 detect\$3) and (diode near1 connected) and capacit\$5	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/01 14:34
S20	25	(photo near1 detect\$3) and (diode near1 connected) and capacit\$5 and "330"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/01 14:42
S21	157	((photo near1 detect\$3) or optical\$5 or light) and ((current adj mirror) same capacit\$5) and "330"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/01 14:43
S24	956	(330/277).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/15 07:43
S31	5	(current adj mirror) and load and (current adj source) and capacit\$5 and "330"/\$.ccls. and (DC or Vdd or (V NEAR1 DD)) and (trans near1 impedance)	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/15 10:15
S32	6	(current adj mirror) and load and (current adj source) and capacit\$5 and (trans near1 impedance) and ((current near3 voltage) same convert\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/15 10:20
S35	360	(current adj mirror) and load and (current adj source) and capacit\$5 and ((current near3 voltage) same convert\$3) and "330"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/15 10:20
S40	327	(330/308 or 330/288).ccls. and ((current near1 mirror) or (diode near1 connected)) and capacit\$5 and load\$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/10/13 16:32


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» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

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|--------------------------|--|
| <input type="checkbox"/> | <p>1. A tiny, high-speed, wide-band, voltage-feedback amplifier stable with all capacitive load
Moraveji, F.;
Solid-State Circuits, IEEE Journal of
Volume 31, Issue 10, Oct. 1996 Page(s):1511 - 1516
Digital Object Identifier 10.1109/4.540063
AbstractPlus References Full Text: PDF(368 KB) IEEE JNL</p> |
| <input type="checkbox"/> | <p>2. Single-point-detection slew-rate enhancement circuits for single-stage amplifiers
Hoi Lee; Mok, P.K.T.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
Volume 2, 26-29 May 2002 Page(s):II-831 - II-834 vol.2
Digital Object Identifier 10.1109/ISCAS.2002.1011482
AbstractPlus Full Text: PDF(569 KB) IEEE CNF</p> |
| <input type="checkbox"/> | <p>3. A high-frequency low-power all-NMOS all-current-mirror sinusoidal quadrature oscillator
Leelasantitham, A.; Srisuchinwong, B.;
TENCON 2004. 2004 IEEE Region 10 Conference
Volume D, 21-24 Nov. 2004 Page(s):364 - 367 Vol. 4
Digital Object Identifier 10.1109/TENCON.2004.1414945
AbstractPlus Full Text: PDF(1861 KB) IEEE CNF</p> |
| <input type="checkbox"/> | <p>4. Low-voltage, low-power and high gain CMOS OTA using active positive feedback with feedforward techniques
Bouzerara, L.; Belaroussi, M.T.; Amirouche, B.;
Microelectronics, 2002. MIEL 2002. 23rd International Conference on
Volume 2, 12-15 May 2002 Page(s):573 - 576
Digital Object Identifier 10.1109/MIEL.2002.1003322
AbstractPlus Full Text: PDF(212 KB) IEEE CNF</p> |
| <input type="checkbox"/> | <p>5. Low-voltage, low-power and high gain CMOS operational transconductance amplifier
Bouzerara, L.; Belaroussi, M.T.;
Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on
Volume 1, 26-29 May 2002 Page(s):I-325 - I-328 vol.1
Digital Object Identifier 10.1109/ISCAS.2002.1009843
AbstractPlus Full Text: PDF(397 KB) IEEE CNF</p> |
| <input type="checkbox"/> | <p>6. A CMOS current-mirror amplifier with compact slew rate enhancement circuit for large capacitive loads</p> |

applications

Hoi Lee; Mok, P.K.T.;

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Digital Object Identifier 10.1109/ISCAS.2001.921830

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- ☐ 7. Analysis of mirror mismatch and clock-feedthrough in Bruton transformation switched current
Al-Hashimi, B.M.; Xie, Y.; Zwolinski, M.;
Circuits, Devices and Systems, IEE Proceedings [see also IEE Proceedings G- Circuits, Devices and
Volume 150, Issue 1, Feb. 2003 Page(s):6 - 15
[AbstractPlus](#) | Full Text: [PDF\(587 KB\)](#) IEE JNL
- ☐ 8. Efficient design of switched-current lowpass elliptic wave filters using Bruton transformation
Lancaster, J.D.; Al-Hashimi, B.M.; Moniri, M.;
Circuits and Systems, 1998. ISCAS '98. Proceedings of the 1998 IEEE International Symposium on
Volume 1, 31 May-3 June 1998 Page(s):115 - 118 vol.1
Digital Object Identifier 10.1109/ISCAS.1998.704201
[AbstractPlus](#) | Full Text: [PDF\(280 KB\)](#) IEEE CNF
- ☐ 9. Design of low-power analog drivers based on slew-rate enhancement circuits for CMOS low
Hoi Lee; Mok, P.K.T.; Ka Nang Leung;
Circuits and Systems II: Express Briefs, IEEE Transactions on [see also Circuits and Systems II: An
Signal Processing, IEEE Transactions on]
Volume 52, Issue 9, Sept. 2005 Page(s):563 - 567
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- ☐ 10. Large bandwidth BICMOS operational amplifiers for SC video applications
Nebel, G.; Kleine, U.; Pfeleiderer, H.-J.;
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Volume 31, Issue 6, June 1996 Page(s):828 - 834
Digital Object Identifier 10.1109/4.509869
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- ☐ 11. Low-voltage CMOS wideband operational transconductance amplifier with regulated cascode
Bouzerara, L.; Belaroussi, M.T.;
Electronics, Circuits and Systems, 2002. 9th International Conference on
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- ☐ 12. A 0.8-V, 8-/spl mu/W, CMOS OTA with 50-dB gain and 1.2-MHz GBW in 18-pF load
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- ☐ 13. Class-AB rail-to-rail CMOS analog buffer
Carrillo, J.M.; Duque-Carrillo, J.F.; Torralba, A.; Carvajal, R.G.;
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